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FIG.1(a)

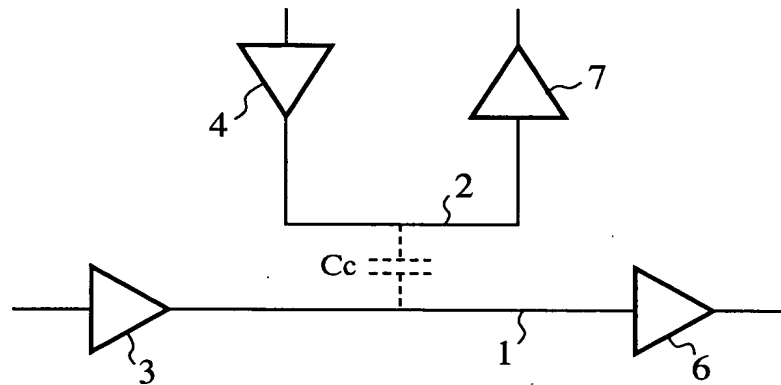


FIG.1(b)
(PRIOR ART)

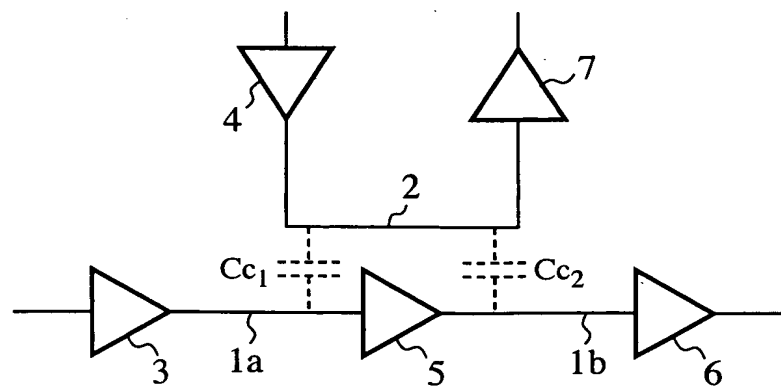


FIG.2

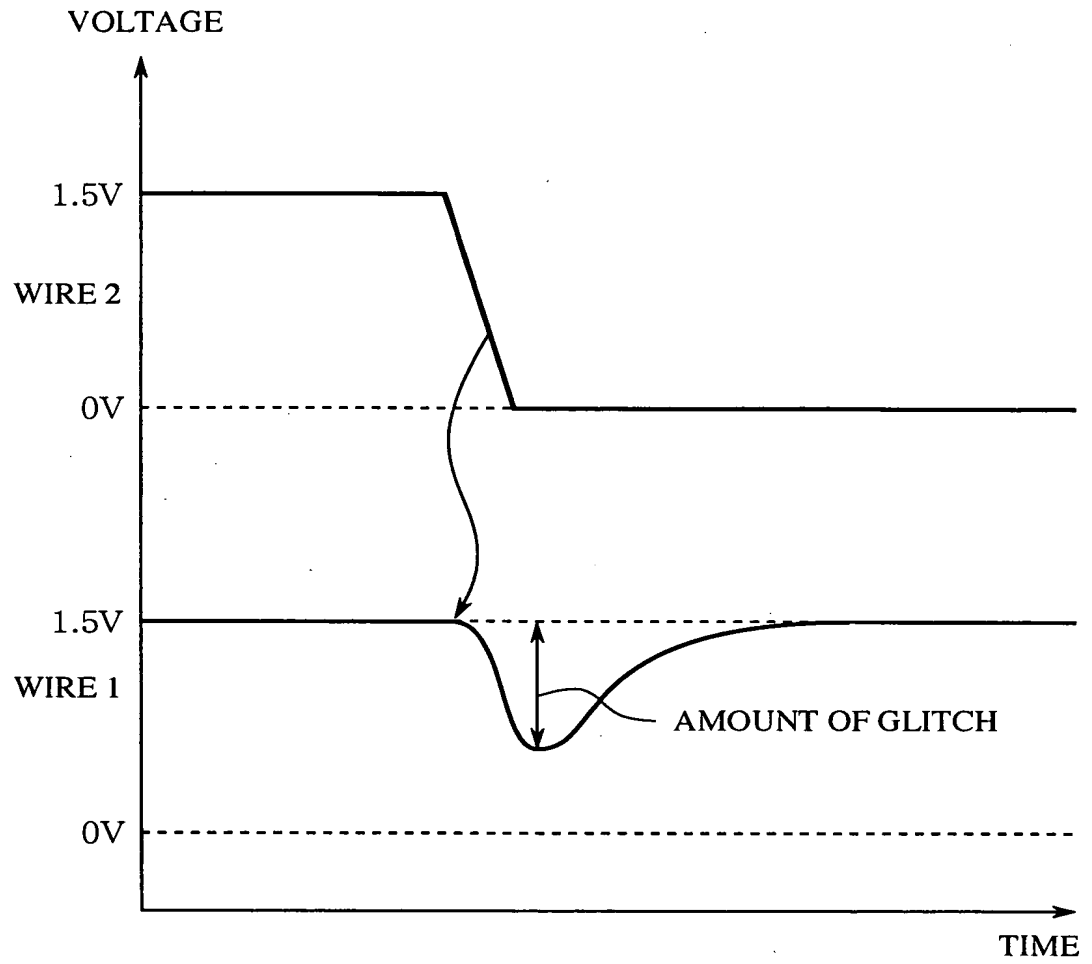


FIG.3 (PRIOR ART)

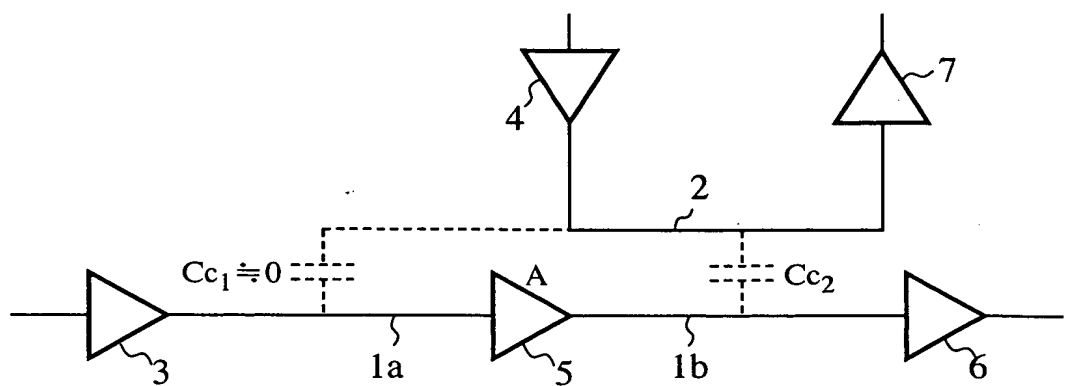


FIG.4(a)

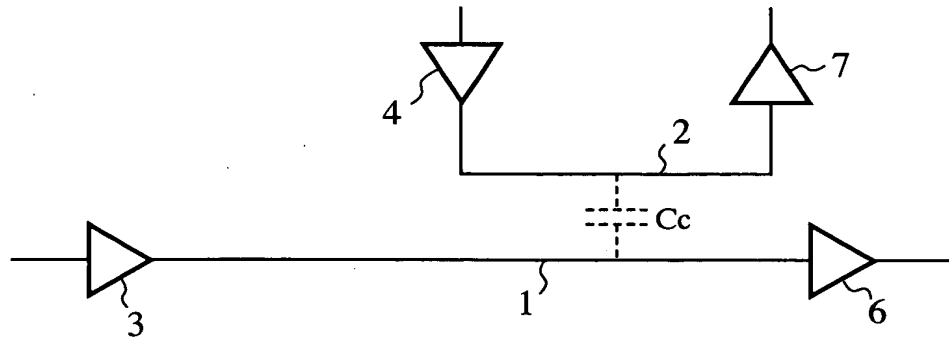


FIG.4(b)

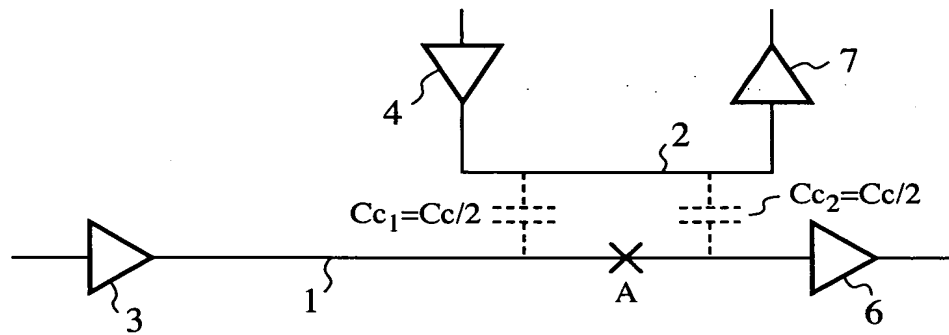


FIG.4(c)

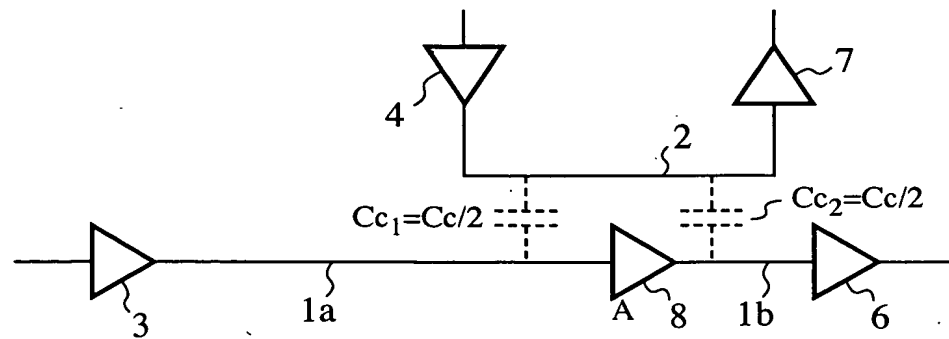


FIG.5(a)

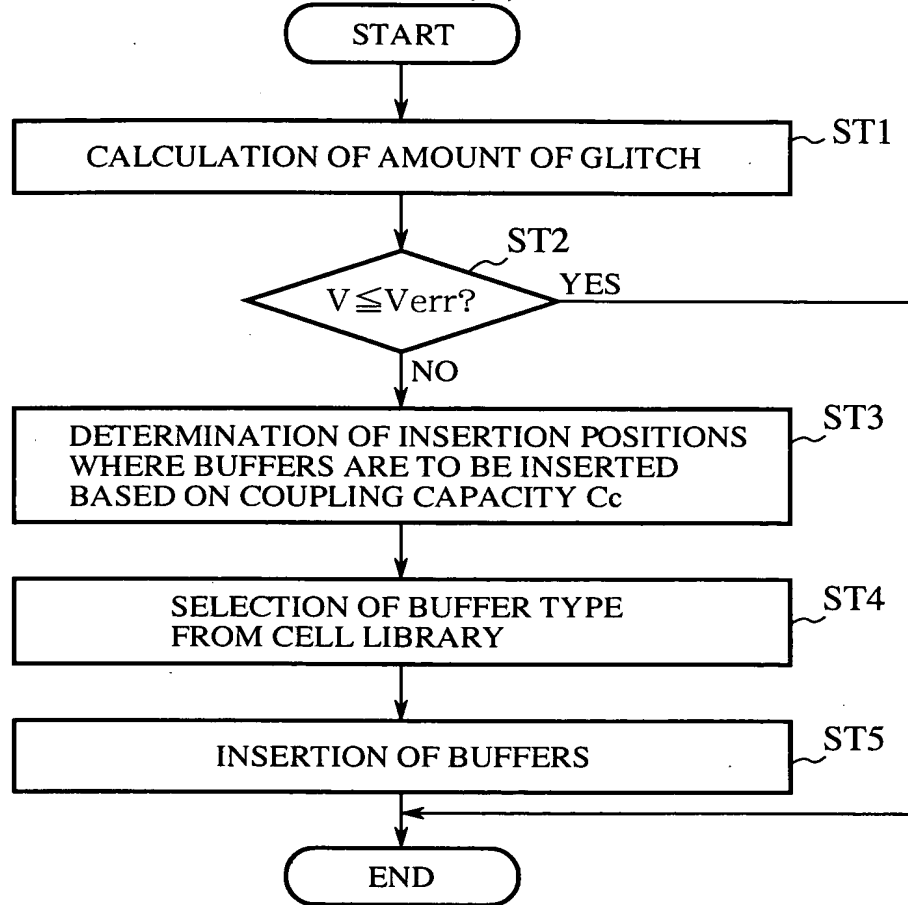


FIG.5(b)

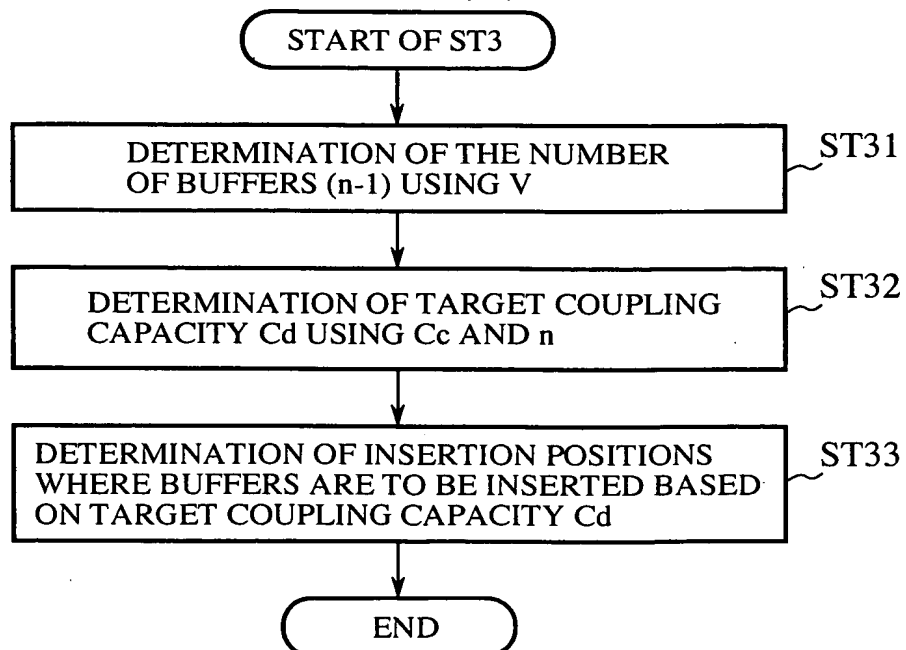


FIG.6

CELL TYPE	PARAMETERS			
	AREA	DRIVING ABILITY
INVERTER 1	$X1(\mu m^2)$	$Y1(\Omega)$.	.
INVERTER 2	$X2(\mu m^2)$	$Y2(\Omega)$.	.
.
.
.
DRIVER 1	$X3(\mu m^2)$	$Y3(\Omega)$.	.
DRIVER 2	$X4(\mu m^2)$	$Y4(\Omega)$.	.
.
.
.
NAND1	$X5(\mu m^2)$	$Y5(\Omega)$.	.
NAND2	$X6(\mu m^2)$	$Y6(\Omega)$.	.
.
.
.
.

FIG.7(a)

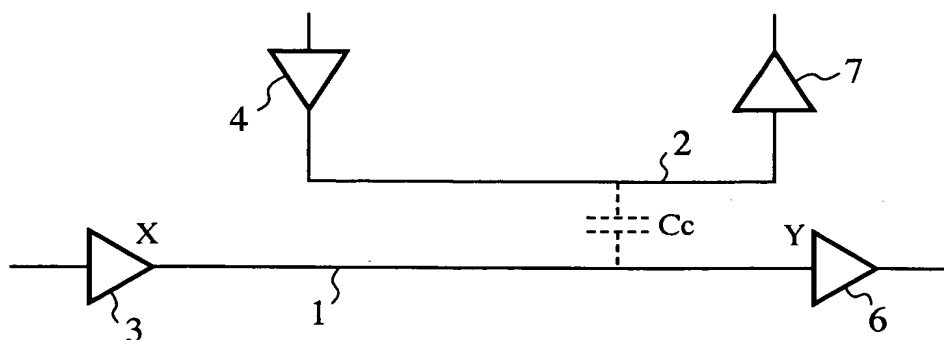


FIG.7(b)

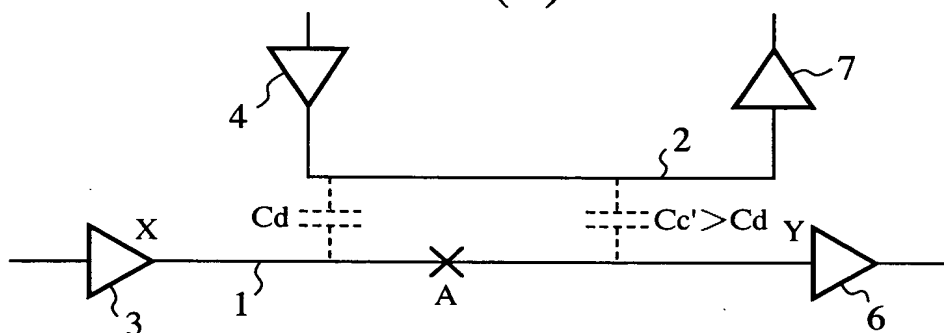


FIG.7(c)

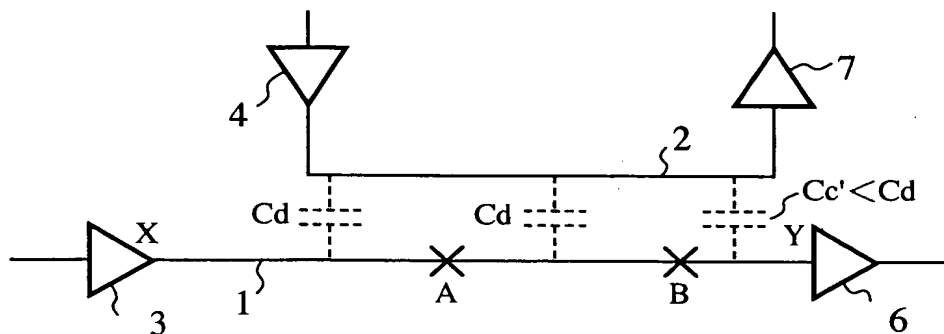


FIG.7(d)

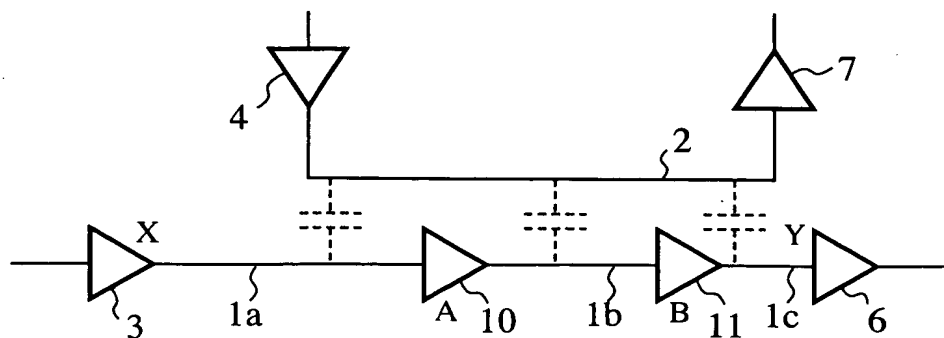


FIG.8(a)

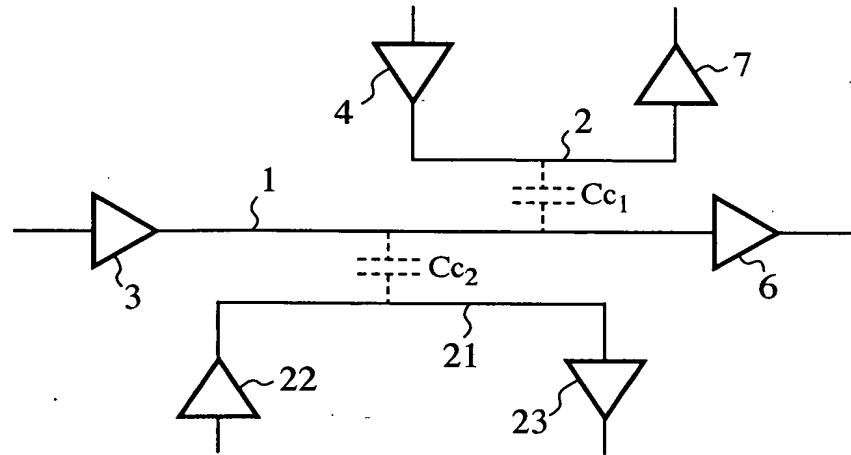


FIG.8(b)

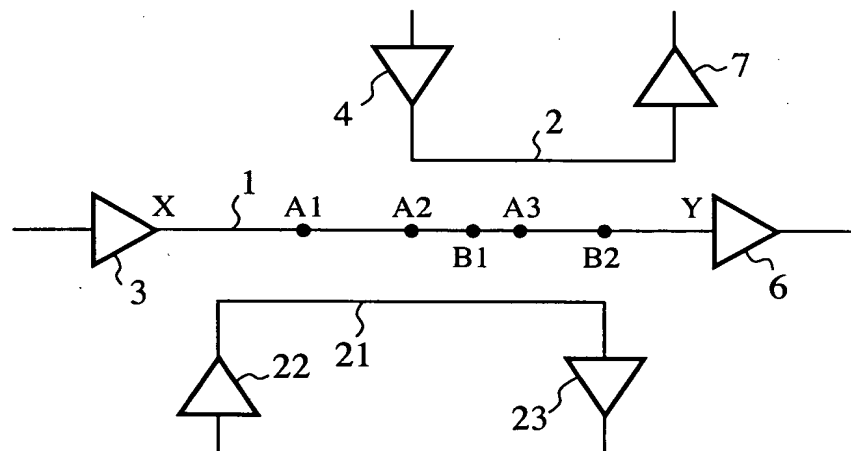


FIG.8(c)

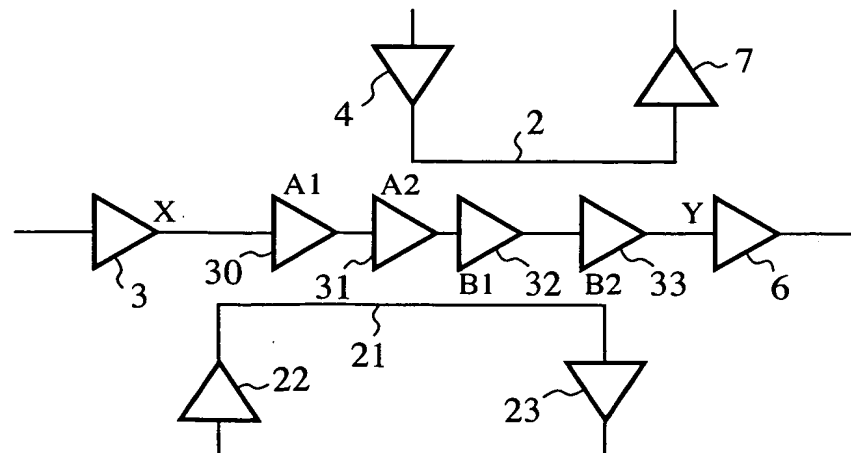


FIG.9(a)

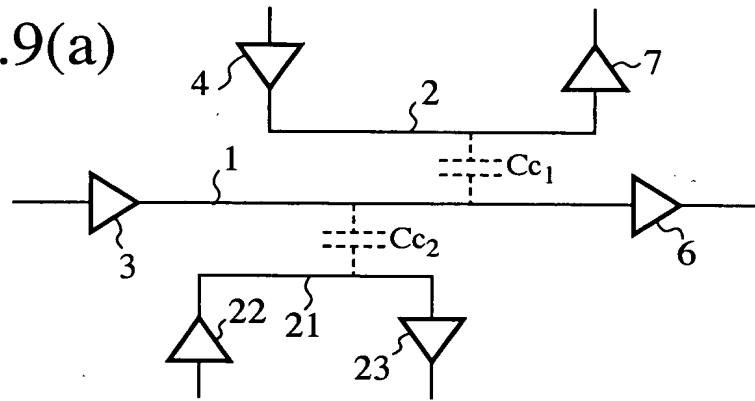


FIG.9(b)

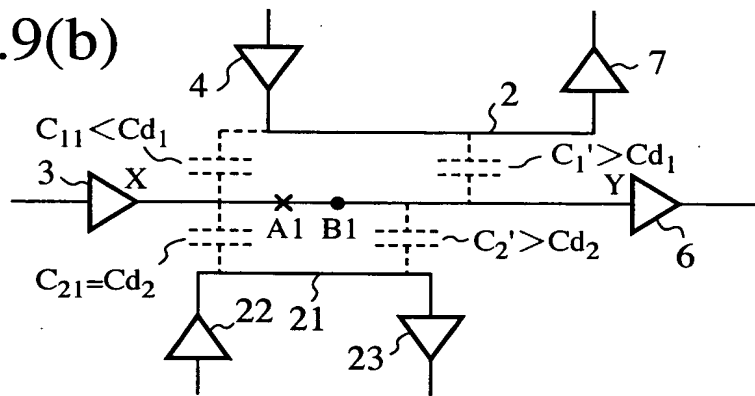


FIG.9(c)

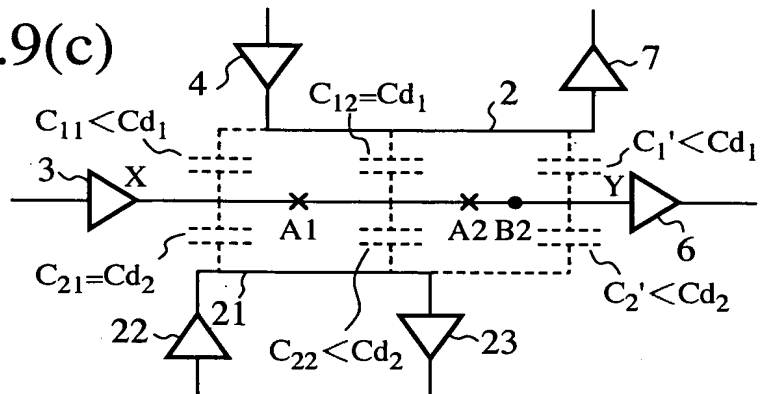


FIG.9(d)

